

**REMARKS****Objection to the Title**

The title was objected to as not being descriptive of the invention. Applicant respectfully traverses this objection.

The Examiner states that a new title is required that is clearly indicative of the invention to which the claims are directed. Independent claims 1 and 9 have a preamble that states "A temperature sensing device". Independent claims 14 and 17 are to memory and electronic devices that comprise "a temperature sensing device". Method claims 19, 23, and 28 are to methods for sensing temperatures in integrated circuits and electronic devices having integrated circuits. Applicant is unclear as to how the present title, "A Temperature Sensing Device in an Integrated Circuit" can be any more descriptive of what is claimed.

**Claim Rejections Under 35 U.S.C. § 102**

Claims 1-27 were rejected under 35 U.S.C. § 102(b) as being anticipated by *Cole et al.* (U.S. Patent No. 6,414,559 and *Peter* (U.S. Patent No. 6,636,937). Applicant respectfully traverses this rejection.

*Cole et al.* disclose a temperature compensated electronic device. *Cole et al.* use a digital temperature sensor, reference 16 in Figure 1 and reference 40 in Figure 2, to sense the temperature of the platform and/or the area immediately surrounding a crystal (col. 3, lines 59 – 61). Applicant's temperature sensing device is comprised of a temperature invariant oscillator, a temperature variant oscillator, and a counter that generates an n-bit count that indicates the temperature in response to the two oscillators. This claimed temperature sensing is neither taught nor suggested by *Cole et al.*

*Peter* discloses a system and method for high temperature operation of a flash memory. The device is comprised of a temperature sensor 18 that is connected to a control unit 16 to which the sensor 18 continually furnishes temperature values (col. 2, lines 54 – 56).

In *Peter*, as in *Cole et al.*, the temperature sensing is performed by a temperature sensor and not a temperature invariant oscillator, a temperature variant oscillator, and a counter that generates a count in response to those oscillators, as claimed in Applicant's claims. Therefore, neither *Peter* nor *Cole et al.* teach or suggest Applicant's invention as claimed in the present claims.

Claims 28 and 29 were rejected under 35 U.S.C. § 102(b) as being anticipated by *Grabon* (U.S. Patent No. 5,912,547). Applicant respectfully traverses this rejection.

*Grabon* discloses a method and apparatus for battery charging. The method comprises reading a control temperature sensor 28 that is enclosed in a casing 31. The casing is located within the apparatus 10 (col. 10, lines 25 – 34). *Grabon* neither teaches nor suggests measuring an ambient temperature with a sensing device that is embedded in a memory circuit, as claimed in Applicant's present claims. Figure 1 of *Grabon* clearly shows that the control temperature sensor 28 is separate from any of the memory devices 18, 19.

**CONCLUSION**

If the Examiner has any questions or concerns regarding this application, please contact the undersigned at (612) 312-2211.

Respectfully submitted,

Date: 11/30/05

  
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